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1 [Don't look back, something's gaining on us: more mixware software engineering metaphor](#)

Dennis E. Hamilton

October 1980 **ACM SIGSOFT Software Engineering Notes**, Volume 5 Issue 4

Full text available: [pdf\(424.03 KB\)](#) Additional Information: [full citation](#)



2 [Don't look back, something's gaining on us: more mixware software engineering metaphor](#)

Dennis E. Hamilton

October 1980 **ACM SIGSOFT Software Engineering Notes**, Volume 5 Issue 4

Full text available: [pdf\(424.03 KB\)](#) Additional Information: [full citation](#)



3 [Accelerating shared virtual memory via general-purpose network interface support](#)

Angelos Bilas, Dongming Jiang, Jaswinder Pal Singh

February 2001 **ACM Transactions on Computer Systems (TOCS)**, Volume 19 Issue 1

Full text available: [pdf\(178.88 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#), [review](#)



Clusters of symmetric multiprocessors (SMPs) are important platforms for high-performance computing. With the success of hardware cache-coherent distributed shared memory (DSM), a lot of effort has also been made to support the coherent shared-address-space programming model in software on clusters. Much research has been done in fast communication on clusters and in protocols for supporting software shared memory across them. However, the performance of software virtual memory (SVM) is sti ...

**Keywords:** applications, clusters, shared virtual memory, system area networks

4 [A high-level microprogrammed processor](#)

Christian Iseli, Eduardo Sanchez

November 1990 **Proceedings of the 23rd annual workshop and symposium on Microprogramming and microarchitecture**

Full text available: [pdf\(587.32 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)




This paper presents a microprogrammable processor, intended for small real-time applications, and the development system associated with it. The processor, designed for an optimal execution of a structured high-level language, has only one level of language: a microcode allowing the execution of three operations in parallel. The possible parallelisms are detected by the compiler, which generates microcode directly. Two other tools are presented: an interactive microassembler and an ...

5 Pipeline Architecture

C. V. Ramamoorthy, H. F. Li

January 1977 **ACM Computing Surveys (CSUR)**, Volume 9 Issue 1


Full text available:  [pdf\(3.53 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

6 The Organization of Microprogram Stores

Subrata Dasgupta

January 1979 **ACM Computing Surveys (CSUR)**, Volume 11 Issue 1


Full text available:  [pdf\(2.07 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

7 Heart: An operating system nucleus machine implemented by firmware

N. Kamibayashi, H. Ogawana, K. Nagayama, H. Aiso

March 1982 **Proceedings of the first international symposium on Architectural support for programming languages and operating systems**, Volume 17, 10 Issue 4, 2

Full text available:  [pdf\(791.96 KB\)](#)


Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper discusses the role of microprogramming in operating system design and shows several things: (1) advantages of the efficiency which may be gained from microcoded operating system primitives, (2) selecting the most appropriate primitives for implementation, and (3) an analysis of the tradeoffs among software, firmware, and hardware. The authors propose a practical approach of enhancing computer architecture level, from a view point of functional hierarchy of operating systems. In o ...

8 Implementation aspects of a SPARC V9 complete machine simulator

Bill Clarke, Adam Czezowski, Peter Strazdins

January 2002 **Australian Computer Science Communications , Proceedings of the twenty-fifth Australasian conference on Computer science - Volume 4**, Volume 24 Issue 1

Full text available:  [pdf\(1.33 MB\)](#)


Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

In this paper we present work in progress in the development of a complete machine simulator for the UltraSPARC, an implementation of the SPARC V9 architecture. The complexity of the UltraSPARC ISA presents many challenges in developing a reliable and yet reasonably efficient implementation of such a simulator. Our implementation includes a heavily object-oriented design for the simulator modules and infrastructure, caching of repeated computations for performance, adding an OS (system call) emu ...

**Keywords:** SMP, SPARC V9 ISA, UltraSPARC, complete machine simulator, execution-driven simulation, object-oriented design

9 Kernel Korner: Linux on Alpha AXP

January 1996 **Linux Journal**

Full text available:  [html\(16.21 KB\)](#)

Additional Information: [full citation](#), [index terms](#)

10 Pen computing: a technology overview and a vision

André Meyer

July 1995 **ACM SIGCHI Bulletin**, Volume 27 Issue 3


Full text available:  pdf(5.14 MB) Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

This work gives an overview of a new technology that is attracting growing interest in public as well as in the computer industry itself. The visible difference from other technologies is in the use of a pen or pencil as the primary means of interaction between a user and a machine, picking up the familiar pen and paper interface metaphor. From this follows a set of consequences that will be analyzed and put into context with other emerging technologies and visions. Starting with a short historic ...

11 Design and realization of MLM: a multilingual machine

F. M G Franca, N. Q. Vasconcelos, E. S T Fernandes

December 1986 **ACM SIGMICRO Newsletter , Proceedings of the 19th annual workshop on Microprogramming**, Volume 17 Issue 4


Full text available:  pdf(986.59 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper describes the design and realization of MLM, a Multi-Lingual machine whose main goal is to provide support for teaching and research in Microprogramming and Computer Architecture. MLM is based on a standard (non-microprogrammable) minicomputer whose microarchitecture has been modified in order to satisfy the requirements of a multi-lingual environment. The resulting machine is microprogrammable, offers facilities for interpreting different target repertoires, and has m ...

12 Platforms: Bluetooth and sensor networks: a reality check

Martin Leopold, Mads Bondo Dydensborg, Philippe Bonnet

November 2003 **Proceedings of the first international conference on Embedded networked sensor systems**

Full text available:  pdf(356.11 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)


The current generation of sensor nodes rely on commodity components. The choice of the radio is particularly important as it impacts not only energy consumption but also software design (e.g., network self-assembly, multihop routing and in-network processing). Bluetooth is one of the most popular commodity radios for wireless devices. As a representative of the frequency hopping spread spectrum radios, it is a natural alternative to broadcast radios in the context of sensor networks. The questio ...

**Keywords:** bluetooth, mac layer, network self-assembly, sensor nodes

13 Microprocessor applications in the nuclear industry

C. Dwayne Ethiridge

April 1980 **ACM SIGCAS Computers and Society**, Volume 10 Issue 3-4

Full text available:  pdf(986.50 KB) Additional Information: [full citation](#), [abstract](#), [references](#)

Microprocessors in the nuclear industry, particularly at the Los Alamos Scientific Laboratory, have been and are being utilized in a wide variety of applications ranging from data acquisition and control for basic physics research to monitoring special nuclear material in long-term storage. Microprocessor systems have been developed to support weapons diagnostics measurements during underground weapons testing at the Nevada Test Site. Multiple single-component microcomputers are now controlling ...

14 An annotated bibliography on microprogramming: An annotated bibliography on microprogramming: late 1969 – early 1972

Louise H. Jones, Kenneth Carvin, Janet Hauser, Paul Herrmann, Frank Pehrson, Harold Reksten, Patricia Van Name


September 1972 **Conference record of the 5th annual workshop on Microprogramming**

Full text available:  [pdf\(873.04 KB\)](#) Additional Information: [full citation](#), [references](#)

15 Toward user sharing of the microprogramming level under UNIX on the Perkin-Elmer 3220

J. Eric Roskos, Robert I. Winner

December 1981 **Proceedings of the 14th annual workshop on Microprogramming**

Full text available:  [pdf\(624.75 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

A master/slave model of writable control store is presented which is claimed to be a better representation of the operating system view of control store than models which more accurately portray the physical reality. Reported work includes the completed development of UNIX tools: an assembler generating relocatable, linkable microcode; a linking, relocating loader; a fast absolute loader; an interactive hard debugging aid allowing the setting of breakpoints; and alterations of the ...

16 Illustrative risks to the public in the use of computer systems and related technology

Peter G. Neumann


January 1996 **ACM SIGSOFT Software Engineering Notes**, Volume 21 Issue 1

Full text available:  [pdf\(2.54 MB\)](#) Additional Information: [full citation](#)

17 Securing wireless applications: ESCORT: a decentralized and localized access control system for mobile wireless access to secured domains

Jiejun Kong, Shirshanka Das, Edward Tsai, Mario Gerla

September 2003 **Proceedings of the 2003 ACM workshop on Wireless security**

Full text available:  [pdf\(401.72 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)


In this work we design and implement ESCORT, a *backward compatible, efficient, and secure* access control system, to facilitate mobile wireless access to secured wireless LANs. In mobile environments, a mobile guest may frequently roam into foreign domains while demanding critical network services. ESCORT provides instant yet secure access to the mobile guest based on the concept of "escort", which refers to a special network object with four distinct properties: (1) T ...

**Keywords:** decentralized access control, identity privacy, location privacy, mobile privacy, wireless security

18 A microprocessor-controlled asynchronous circuit switching network

Tse-yun Feng, Chuan-lin Wu, Dharma P. Agrawal

April 1979 **Proceedings of the 6th annual symposium on Computer architecture**

Full text available:  [pdf\(839.81 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper describes an asynchronous circuit switching network for multiple-processor systems. Several circuit switching networks for various applications have been proposed and constructed. However, there are problems associated with these networks. The


asynchronous circuit switching network possesses several features that can solve these problems. A three-stage fully connected topology is utilized to construct the network. Each switching element is functionally and physically identical an ...

## 19 Features: Modern System Power Management

Andrew Grover

October 2003 **Queue**, Volume 1 Issue 7

Full text available:  [pdf\(898.29 KB\)](#)

 [html\(24.20 KB\)](#)

Additional Information: [full citation](#), [index terms](#)



## 20 Emulation of computer networks by microprogrammable microcomputers

David Cohen, Ming T. Liu

September 1974 **Conference record of the 7th annual workshop on Microprogramming**

Full text available:  [pdf\(430.27 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The advent of low cost, sophisticated, microprogrammable, LSI microprocessors has renewed interest in multiple-computer systems. This paper suggests a method of implementing microprogrammable microcomputer systems as a sophisticated tool (emulators) for decreasing the economic risk involved in development of large computer networks. Two levels of emulation are proposed for different network configurations. At the first level each microprocessor emulates one of the large computers in the rea ...



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[Blurring the Line Between OSES and Storage Devices - Ganger \(2001\)](#) (Correct) (5 citations)  
collectively. Similarly, schemes like soft **updates** [17] and RIO [11] allow aggressive write-back together with extended versions of today's OS and **firmware** specializations, would allow the two to  
[www.pdl.cmu.edu/Publications/./ftp/Storage/CMU-CS-01-166.ps](http://www.pdl.cmu.edu/Publications/./ftp/Storage/CMU-CS-01-166.ps)

[Trapeze/IP: TCP/IP at Near-Gigabit Speeds - Gallatin, Chase, Yocum](#) (Correct) (26 citations)  
I/O, checksum offloading, message pipelining, and **interrupt** suppression. Our experiments use extended which consists of a messaging library and custom **firmware** for Myrinet. Using Trapeze **firmware**, Myrinet  
[www.cs.duke.edu/ari/publications/tcpgig.ps](http://www.cs.duke.edu/ari/publications/tcpgig.ps)

[Disrupt and Interrupt in MSC: possibilities and problems - Cobben, Engels \(1998\)](#) (Correct)  
**Interrupt** and Disrupt in MSC: Possibilities and Problems  
[ftp.win.tue.nl/pub/techreports/engels/interdis.ps.gz](http://ftp.win.tue.nl/pub/techreports/engels/interdis.ps.gz)

[Two-Arm Trajectory Planning in a Manipulation Task - Garvin, Zefran, Henis, Kumar \(1997\)](#) (Correct) (2 citations)  
reaching motions in the horizontal [Morasso 1981 **Flash** and Hogan 1985 Uno et al. 1989] and vertical [where  $x_i$  is the  $i$ th component of the state **vector**  $x$  and  $n$  is the highest order derivative of  $x_i$ ]  
[ftp.cis.upenn.edu/pub/kumar/papers/1997/GZHK\\_jbc.ps](http://ftp.cis.upenn.edu/pub/kumar/papers/1997/GZHK_jbc.ps)

[SoftSDV: A Presilicon Software Development Environment .. - Uhlig, Fishtein.. \(1999\)](#) (Correct) (4 citations)  
Microprocessor Research Labs, Oregon, Intel Corp. Roman Fishtein, MicroComputer Products Lab, Haifa, functions (e.g. virtual-memory management, **interrupt** processing, etc. Not only do requirements  
If the translation succeeds, the V2H table is **updated**, and execution returns to the capsule. If the  
[developer.intel.com/technology/itj/q41999/articles/./pdf/softsdv.pdf](http://developer.intel.com/technology/itj/q41999/articles/./pdf/softsdv.pdf)

[A Pilot Project on Module Testing for Embedded Software - McDonald, Murray.. \(2000\)](#) (Correct)  
of this paper. References 1] V.R. Basili and H.D. Rombach. The TAME project: towards management, critical section entry/exit, and **interrupt** disable/enable functions that are provided by creates and initialises a digital input object, **UpdateDigital**(modifies the state of a digital input  
[svrc.it.uq.edu.au/techreports/tr00-24.ps.gz](http://svrc.it.uq.edu.au/techreports/tr00-24.ps.gz)

[ESP: A Language for Programmable Devices - Kumar, Mandelbaum, Yu, Li \(2001\)](#) (Correct)  
request for a message to be sent SendReq or to **update** the virtual to physical translation **UpdateReq**. C file that can be used to generate efficient **firmware** for the device and a specification that can be M. Heinrich. Using meta-level compilation to check **flash** protocol code. In Architectural Support for  
[www.cs.princeton.edu/~skumar/papers/pldi2001.ps](http://www.cs.princeton.edu/~skumar/papers/pldi2001.ps)

[PCI Bus Binding to: IEEE Std 1275-1994 Standard for Boot.. - This Foreword Is](#) (Correct)  
of the Specification. Introduction **Firmware** is the **ROM**-based software that controls a computer between requirements and practices for address format, **interrupts**, probing, and related properties and methods. Standard for Boot (Initialization Configuration) **Firmware** Revision 2.1 Foreword by the Chairman of the  
[banana.r6000.apple.com/1275/bindings/pci/pci2\\_1.pdf](http://banana.r6000.apple.com/1275/bindings/pci/pci2_1.pdf)

[Embedded System Design - Ecen Spring Lectures](#) (Correct)  
systems. Floppy and hard disk drives, CD-RW and DVD-ROM drives, and external peripherals such as printers, keypad decoding, 8051 timers/counters. **Interrupts** and **Interrupt** Service Routines (ISRs) in the order given or on the dates shown. **Updates** will be made as the course progresses.  
[ece-www.colorado.edu/~mclurel/s03syllabus.pdf](http://ece-www.colorado.edu/~mclurel/s03syllabus.pdf)

[A Smart Camera for Traffic Surveillance - Michael Bramberger Roman](#) (Correct)



for Traffic Surveillance Michael Bramberger, Roman P. Pflugfelder Arnold Maier Bernhard at which time the background model should be **updated**. We propose a background/foreground and finally (iv) the camera control and **firmware**. Video Compression To reduce the required  
[www.vmars.tuwien.ac.at/wises/papers/12\\_A\\_smart\\_traffic\\_camera\\_for\\_stationary\\_vehicle\\_detection.pdf](http://www.vmars.tuwien.ac.at/wises/papers/12_A_smart_traffic_camera_for_stationary_vehicle_detection.pdf)

PC Card Binding to IEEE Standard 1275-1994, Standard for... - Draft Revision Date (Correct)  
Not For Publication Introduction **Firmware** Is The **Rom**-Based Software That Controls A Computer Between requirements and practices for address format, **interrupts**, probing, and related properties and methods. Revised 03/09/95 1:36 Pm Pc Card Binding Open **Firmware** Draft Document 1 Task Force Not For  
[playground.sun.com/1275/bindings/pccard/pccd1\\_2.ps](http://playground.sun.com/1275/bindings/pccard/pccd1_2.ps)

Continuous Flash - Hugues Hoppe Kentaro (Correct)  
(that do pass through the points, e.g. Catmull-**Rom** spline)**Flash** adjustment scenarios **Flash** camera itself, using appropriate software or **firmware**. Or, it can occur later when the images are  
Page 1 of 7 Continuous **Flash** Hugues Hoppe Kentaro Toyama October 1, 2003  
[www.research.microsoft.com/%7Ehoppe/contflash.pdf](http://www.research.microsoft.com/%7Ehoppe/contflash.pdf)

Media Access Control Development Platform for Wireless LANs - Ganz, Savvides, Ganz (Correct)  
have to be familiar with the network card **firmware** as well as with the device drivers coding controller chip. These cards carry 128Kbytes of **flash** memory and 128Kbytes of SRAM memory. The internal will use the command number to index the function **vector** table and execute the appropriate function. The  
[dvd1.ecs.umass.edu/wireless/publications/multimedia/dev1099.pdf](http://dvd1.ecs.umass.edu/wireless/publications/multimedia/dev1099.pdf)

The Unimation Puma servo system - Corke (1994) (Correct) (2 citations)  
:15 2.2.2 **Interrupts** :  
counter is read and a 24-bit software counter **updated**. On encoder index (once per motor revolution)  
: 18 2.3 Digital servo board **firmware** :  
[janus.cat.csiro.au/pub/pic/pumaservo.ps.Z](http://janus.cat.csiro.au/pub/pic/pumaservo.ps.Z)

Cipher Instruction Search Attack on the Bus-Encryption Security.. - Kuhn (1998) (Correct)  
bytes of program memory, including the reset and **interrupt vectors**, in an on-chip "**vector** RAM. Whenever CPU pin, the system manufacturer activates a **firmware** monitor in the processor, which generates a new program memory, including the reset and **interrupt vectors**, in an on-chip "**vector** RAM. Whenever the CPU  
[www3.informatik.uni-erlangen.de/Publications/Articles/kuhn\\_ToC.pdf](http://www3.informatik.uni-erlangen.de/Publications/Articles/kuhn_ToC.pdf)

Investigating Open Source Software - And Educational Robotics (Correct)  
"brick" is a Hitachi H8 microcontroller with 16K of **ROM** and 32K of RAM. Programs written on a PC can be to the RCX. The programs rely on RAM resident **firmware** and a "System **ROM**" to run routines. 3] While  
[elvis.rowan.edu/~kay/papers/OSSEduRob.pdf](http://elvis.rowan.edu/~kay/papers/OSSEduRob.pdf)

SCAMPI - A Scaleable Monitoring Platform for the Internet - Jan Coppens Imec (2004) (Correct)  
some monitoring function in its hardware or **firmware**, MAPI will automatically use it. If it is not  
[www.ist-scampi.org/publications/papers/coppens-ips2004.pdf](http://www.ist-scampi.org/publications/papers/coppens-ips2004.pdf)

IEEE October 2001 57 - Cover Feature Building (Correct)  
of randomness and minimal software within the 4758's **ROM**. When shipped, each 4758 can authenticate requests via a 16-byte agent ID. We used mailboxes and **interrupts** to implement the protocol with a host device of initial facilities using the secure field **update** architecture. Self-initialization A  
[www.princeton.edu/~rblee/ELE572Papers/IBM4758copr.pdf](http://www.princeton.edu/~rblee/ELE572Papers/IBM4758copr.pdf)

Unknown - Vfltrbw Ffltr Cvabs (Correct)  
enable register IIER .word 0h Internal **interrupt** enable register IIFR .word 00002202h Flag  
} Menu callback function \*void CVICALLBACK UpdateCB(int menunubar, int menuitem, void \*callbackData,  
After loading all the real-time signal processing **firmware** and software, as listed in Appendix A.1, on the  
[scholar.lib.vt.edu/theses/available/etd-72197-14559/unrestricted/etdb.pdf](http://scholar.lib.vt.edu/theses/available/etd-72197-14559/unrestricted/etdb.pdf)

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### 1 A realtime telecommunication platform geared to advanced online maintenance

*Futagami, S.; Araki, H.; Okada, K.;*

Global Telecommunications Conference, 1994. GLOBECOM '94. 'Communications: The Global Bridge', IEEE, 28 Nov.-2 Dec. 1994  
Pages:577 - 581 vol.1

[\[Abstract\]](#)   [\[PDF Full-Text \(448 KB\)\]](#)   IEEE CNF

### 2 SunRay: a cost-effective desktop computer solution

*Tougaw, D.; Sanders, J.;*

Computing in Science & Engineering [see also IEEE Computational Science and Engineering], Volume: 4, Issue: 1, Jan.-Feb. 2002  
Pages:15 - 17

[\[Abstract\]](#)   [\[PDF Full-Text \(398 KB\)\]](#)   IEEE JNL

### 3 A versatile framework for FPGA field updates: an application of partial self-reconfiguration

*Fong, R.J.; Harper, S.J.; Athanas, P.M.;*

Rapid Systems Prototyping, 2003. Proceedings. 14th IEEE International Workshop on, 9-11 June 2003  
Pages:117 - 123

[\[Abstract\]](#)   [\[PDF Full-Text \(320 KB\)\]](#)   IEEE CNF

### 4 PC supported motor drive controller

*Drevensek, D.; Urlep, E.; Curkovic, M.;*

Industrial Electronics, 1999. ISIE '99. Proceedings of the IEEE International Symposium on, Volume: 2, 12-16 July 1999

Pages:647 - 650 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(500 KB\)\]](#) [IEEE CNF](#)

---

**5 TMS320C30 DSP based implementation of a half rate CELP coder**

*Wang, Y.-S.; McCarthy, B.M.;*

Acoustics, Speech, and Signal Processing, 1992. ICASSP-92., 1992 IEEE

International Conference on , Volume: 4 , 23-26 March 1992

Pages:369 - 372 vol.4

[\[Abstract\]](#) [\[PDF Full-Text \(304 KB\)\]](#) [IEEE CNF](#)

---

**6 On fast firmware/software-based video coding**

*Weisi Lin;*

Consumer Electronics, IEEE Transactions on , Volume: 48 , Issue: 2 , May 2002

Pages:209 - 219

[\[Abstract\]](#) [\[PDF Full-Text \(1104 KB\)\]](#) [IEEE JNL](#)

---

**7 Universal serial bus enhances virtual instrument-based distributed power monitoring**

*Chung-Ping Young; Devaney, M.J.; Shyh-Chyang Wang;*

Instrumentation and Measurement, IEEE Transactions on , Volume: 50 , Issue:

6 , Dec. 2001

Pages:1692 - 1697

[\[Abstract\]](#) [\[PDF Full-Text \(190 KB\)\]](#) [IEEE JNL](#)

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**8 Design and implementation of the POWER5/spl trade/ microprocessor**

*Clabes, J.; Friedrich, J.; Sweet, M.; DiLullo, J.; Chu, S.; Plass, D.; Dawson, J.; Muench, P.; Powell, L.; Floyd, M.; Sinharoy, B.; Lee, M.; Goulet, M.; Wagoner, J.; Schwartz, N.; Runyon, S.; Gorman, G.; Restle, P.; Kalla, R.; McGill, J.; Dodson, S.;*

Integrated Circuit Design and Technology, 2004. ICICDT '04. International Conference on , 17-20 May 2004

Pages:143 - 145

[\[Abstract\]](#) [\[PDF Full-Text \(329 KB\)\]](#) [IEEE CNF](#)

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**9 Dynamic network management for firmware controlled network topology**

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